

	Type	L #	Hits	10/064,265 Search Text 1-3-06
1	BRS	L1	9	lee-kuan-yu.in.
2	BRS	L2	22	lee-chen-ho.in.
3	BRS	L3	16	2 not 1
4	BRS	L4	136	(timing adj generat\$3) with (CCD (charge\$2 adj coupl\$2 adj device)) with analog\$2 with ((A adj D) (analog\$2 adj digital) digitiz\$5)
5	BRS	L5	5	4 same scanner
6	BRS	L6	131	4 not 5
7	BRS	L7	73	6 and (image document original manuscript object draft).ti.
8	BRS	L8	58	6 not 7
9	BRS	L9	1	8 and (scan\$5 read\$3).ti.
10	BRS	L10	161	(timing adj2 generat\$3) with (CCD (charge\$2 adj coupl\$2 adj device)) with analog\$2 with ((A adj D) (analog\$2 adj digital) digitiz\$5)
11	BRS	L11	30	10 not 6
12	BRS	L12	42	(timing adj2 generat\$3) and 358/483.ccls.
13	BRS	L13	29	(timing adj2 generat\$3) and 358/482.ccls.
14	BRS	L14	22	13 not 12
15	BRS	L15	0	(timing adj2 generat\$3) with (CCD (charge\$2 adj coupl\$2 adj device)) with analog\$2 with ((A adj D) (analog\$2 adj digital) digitiz\$5) with ("same" adj3 (board circuit substrate module))
16	BRS	L16	3	(timing adj2 (control\$3 generat\$3)) with analog\$2 with ((A adj D) (analog\$2 adj digital) digitiz\$5) with ("same" adj3 (board circuit substrate module))
17	BRS	L17	24657	(main adj2 (circuit board)).ab.

	Type	L #	Hits	Search Text
18	BRS	L18	213	(main adj2 (circuit board)) with (sensor adj2 (circuit board))
19	BRS	L20	1	18 and ((sensor adj2 (circuit board)) with ((A adj D) (analog\$2 adj digital) digiti\$6) with timing)
20	BRS	L19	17	18 and ((sensor adj2 (circuit board)) with ((A adj D) (analog\$2 adj digital) digiti\$6))
21	BRS	L21	20744	(control adj board) with (sensor board)
22	BRS	L22	58	(control adj board) with (sensor adj board)
23	BRS	L23	51080	(main adj control adj\$2 (circuit board module)) with ((sensor ccd) adj2 (circuit board module))
24	BRS	L24	52406	23 andd (((sensor ccd) adj2 (circuit board module)) with timing with ((A adj D) (analog adj digital) digiti\$6))
25	BRS	L25	106	23 and (((sensor ccd) adj2 (circuit board module)) with timing with ((A adj D) (analog adj digital) digiti\$6))
26	BRS	L26	17	25 and scanner
27	BRS	L27	107	((sensor ccd (photoelectric adj conver\$6)) adj2 (circuit board module substrate)) with timing with ((A adj D) (analog adj digital) digiti\$6)
28	BRS	L28	51	27 and (image document original manuscript object draft scan\$5 read\$3).ti.
29	BRS	L29	56	27 not 28
30	BRS	L30	5	29 and sensor.ti.
31	BRS	L31	51	29 not 30
32	BRS	L32	832	(timing adj2 generat\$3) with (sensor CCD) with ((A adj D) ((photoelectric (photo adj electric)) adj conver\$5) (analog\$2 adj digital) digiti\$6)
33	BRS	L33	789	(timing adj2 generat\$3) with (sensor CCD ((photoelectric (photo adj electric)) adj conver\$6)) with ((A adj D) (analog\$2 adj digital) digiti\$6)

	Type	L #	Hits	Search Text
34	BRS	L34	440	33 and (image document original manuscript object draft scan\$5 read\$3 sensor).ti.
35	BRS	L35	90	34 and "358"/\$.ccls.
36	BRS	L36	454	33 with (module circuit board substrate)
37	BRS	L37	54	36 and "358"/\$.ccls.
38	BRS	L38	400	36 not 37
39	BRS	L39	9	38 and 250/208.1.ccls.
40	BRS	L40	391	38 not 39
41	BRS	L41	12	40 and "250"/\$.ccls.
42	BRS	L42	379	40 not 41
43	BRS	L43	158	42 not "348"/\$.ccls.
44	BRS	L44	164	wada-shinichiro.in.
45	BRS	L45	19	44 and timing
46	BRS	L46	0	Miyashita-Mickiaki.in.
47	BRS	L47	5	Miyashita-Mikiaki.in.
48	BRS	L48	9	"5714752".uref.
49	BRS	L49	1550	"358"/\$.ccls. and usb
50	BRS	L50	12	49 and 358/483.ccls.
51	BRS	L51	122	49 and 358/474.ccls.
52	BRS	L52	118	51 not 50
53	BRS	L53	118	52 and 358/474.ccls.
54	BRS	L54	20	arafune-shoji.in.

	Type	L #	Hits	Search Text
55	BRS	L55	229	(main adj2 (circuit board)) with ((CCD sensor) adj2 (circuit board))
56	BRS	L56	14	55 and scanner
57	BRS	L57	0	56 not 55
58	BRS	L58	215	55 not 56
59	BRS	L59	208	58 not "348"/\$.ccls.
60	BRS	L60	113	nishio-kenichi.in.
61	BRS	L61	2	60 and (ccd adj circuit adj board)
62	BRS	L62	2	60 and (main adj circuit adj board)
63	BRS	L63	0	60 and (main adj circuit adj module)
64	BRS	L64	33	mai-che-kuei.in.
65	BRS	L65	12	64 and board
66	BRS	L66	5	"6002508".uref.
67	BRS	L67	11	chang-martin.in.
68	BRS	L68	9	mainboard same scanner
69	BRS	L69	42	((control adj board) same ((sensor ccd) adj board)) and scanner
70	BRS	L70	596	(main adj2 circuit) and ccd
71	BRS	L71	48	70 and (timing adj2 generat\$3)
72	BRS	L72	548	70 not 71
73	BRS	L73	152	72 and scanner
74	BRS	L74	570	(tim\$3 adj2 (circuit generat\$3)). with (AD (A adj D) (analog\$2 adj digital) digiti\$6) with ccd

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75	BRS	L75	581	(tim\$3 adj2 (circuit generat\$3)) with (AD (A adj D) (analog\$2 adj digital) digiti\$6) with (ccd (charge\$2 adj couple\$2))
76	BRS	L76	106	75 and scanner
77	BRS	L77	420	75 with (board circuit module substrate)
78	BRS	L78	49	77 and scanner
79	BRS	L79	34	iwai-kenichi.in.
80	BRS	L80	5	79 and timing
81	BRS	L81	3	"6204910".uref.
82	BRS	L82	29	79 not 80
83	BRS	L83	371	77 not 78
84	BRS	L84	207	358/482.ccls. and timing
85	BRS	L85	157	358/482.ccls. and timing and (AD (A and D) (analog\$2 adj digital) digiti\$6)
86	BRS	L86	82	358/514.ccls. and timing and (AD (A and D) (analog\$2 adj digital) digiti\$6)
87	BRS	L87	76	86 not 85
88	BRS	L88	780	(control adj2 board).ab. and (image document original scan\$5 read\$3).ti.
89	BRS	L89	17	88 and "358"/\$.ccls.
90	BRS	L90	17	89 and (control adj2 board)
91	BRS	L91	449	matsumoto-yasuhiro.in.
92	BRS	L92	4	91 and (control adj2 board)
93	BRS	L93	92	nose-yoshitaka.in.
94	BRS	L94	1	93 and (control adj2 board)

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95	BRS	L95	2018	yamamoto-koji.in.
96	BRS	L96	3	95 and (control adj2 board)
97	BRS	L97	15	scanner with (main adj circuit adj board)
98	BRS	L98	21	scanner with (main adj circuit)
99	BRS	L99	6	98 not 97
100	BRS	L100	725	scanner and (((tim\$3 clock) adj3 generat\$3) with (AD (A adj D) (analog\$2 adj digital) digiti\$6))
101	BRS	L101	328	scanner and (((tim\$3 clock) adj3 generat\$3) with (AD (A adj D) (analog\$2 adj digital) digiti\$6) with (board substrate circuit module))
102	BRS	L102	23	((tim\$3 clock) adj3 generat\$3) with (AD (A adj D) (analog\$2 adj digital) digiti\$6) with ("same" adj3 (board substrate circuit module))
103	BRS	L103	4502	((tim\$3 clock) adj3 generat\$3) with (AD (A adj D) (analog\$2 adj digital) digiti\$6) with (board substrate circuit module)
104	BRS	L104	8	103 and 358/482.ccls.
105	BRS	L105	4494	103 not 104
106	BRS	L106	20	105 and 358/474.ccls.
107	BRS	L107	4474	105 not 106
108	BRS	L108	0	107 and 358/482.ccls.
109	BRS	L109	3	107 and 358/497.ccls.
110	BRS	L110	0	108 not 109
111	BRS	L111	4471	107 not 109
112	BRS	L112	28	111 and 250/208.1.ccls.

	Type	L #	Hits	Search Text
113	BRS	L113	4443	111 not 112
114	BRS	L114	4	113 and 250/234-236.ccls.
115	BRS	L115	0	113 and 399/211.ccls.
116	BRS	L116	1906	scanner and (motherboard (mother adj board))
117	BRS	L117	127	scanner and ((motherboard (mother adj board)) same (ad (a adj d) (analog\$2 adj digital) digiti\$6))
118	BRS	L118	85	((timing adj3 generat\$3) with (sensor CCD (charge\$2 adj couple\$2)) with (ad (a adj d) (analog\$2 adj digital) digiti\$6)).ab.
119	BRS	L119	54	maki-yasuhito.in.
120	BRS	L120	3	119 and scanner
121	BRS	L121	51	119 not 120
122	BRS	L122	25	121 and (ad (a and d) (analog\$2 adj digital))
123	BRS	L123	22	122 and timing
124	BRS	L124	3	"5717459".uref.
125	BRS	L125	335	goto-tetsuro.in.
126	BRS	L126	29	125 and (ad (a and d) (analog\$2 adj digital)) and timing
127	BRS	L127	26	126 not 123